EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	117	asynchronous$2 near12 clock$3 with peripheral with (device or unit or modules or circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/03 11:45
S21	49	S20 and (chip or IC)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/02 10:15
S29	19527	(CPU or core) near4 clock	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/02 10:15
S30	988	S29 same ((chip or peripheral) near3 clock)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/02 10:16
S31	124	S30 same synchroniz$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/02 10:20
S32	83	S31 and @ad < "20020601"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/02 10:21
S33	24	S32 and ((first near3 clock) same (second near3 clock))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/02 12:32
S34	2	("6934870").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2006/11/02 12:35

S35	2	("6079022").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2006/11/02 17:32
S50	17	PCI near3 device near5 clock near5 input	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/03 12:33
S51	578	oscillator near5 card	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/03 12:33
S52	47	S51 with CPU	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/03 13:12
S53	0	PCI near3 device near5 controllable near6 oscillator	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/03 13:13
S54	0	PCI near3 device with controllable near6 oscillator	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/03 13:13
S55	0	PCI near3 (device or interface) with controllable near6 oscillator	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/03 13:15
S57	0	S56 and (controllable near3 oscillator)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/03 13:15
S58	53	PCI near10 oscillator	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/03 13:16

S59	0	S58 same (controllable near3 oscillator)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/03 13:15
S60	2679	controllable near3 oscillator	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/03 13:31
S61	0	S60 with PCI	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/03 13:17
S62	0	S60 same PCI	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/03 13:17
S63	2679	S60 S60 with device	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/03 13:17
S64	272	S60 with device	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/03 13:19
S65	12	S64 and peripher$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/03 13:31
S66	2	S60 with (peripher$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/03 13:22
S67	5	S60 with CPU	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/03 13:26

S68	9	S60 same CPU	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/03 13:26
S69	41487	(controllable near3 oscillator) or VCO	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/03 13:31
S70	61	S69 with peripher$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/03 13:33
S71	4200	S69 with (PCI near3 card or device or interface)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/03 13:33
S72	0	S69 with (PCI near3 (card or device or interface))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/03 13:33
S73	26	S69 with PCI	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/03 13:45
S74	0	S69 near20 (PCI near (CLK or CLOCK))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/03 13:39
S75	0	S69 same (PCI near (CLK or CLOCK))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/03 13:39
S76	870	S69 same (device near6 (connect$2 or coupl$2))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/03 13:53

S77	317	S69 with (device near6 (connect$2 or coupl$2))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/03 13:52
S78	224	S69 near8 (device near6 (connect$2 or coupl$2))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/03 13:52
S79	2	S69 with ((peripheral near2 device) near6 (connect$2 or coupl$2))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/03 13:49
S80	10	S69 same ((peripheral near2 device) near6 (connect$2 or coupl$2))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/03 13:49
S81	3	S78 and PCI	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/03 13:52
S82	0	S77 with PCI	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/03 13:53
S83	5	S77 and PCI	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/03 13:53
S84	16	S76 and PCI	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/03 13:59
S85	110	vco near8 (coupled or connected) with (card or chip or peripheral or PCI)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/03 14:01

S86	93	S85 and @ad < "20020202"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/03 15:06
S87	3	PLL near4 (connected or coupled) near7 PCI	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/03 15:11
S88	0	S87 and VCO	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/03 15:11
S89	5	PLL near8 (connected or coupled) near7 PCI	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	.2006/11/03 15:11
S90	0	S89 and VCO	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/03 15:11
S91	5	PLL near10 (connected or coupled) near8 PCI	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/11/03 15:12